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Attorney for Applicant

Date: 25 May 2007



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.	:	10/804,699
Applicant	:	BALES, Tim J.
Filed	:	19 March 2004
Title	:	MOS Linear Region Impedance Curvature Correction
Art Unit	:	2819
Examiner	:	CHANG, Daniel D.
Atty. Docket No.	:	PAT001094-000

AMENDMENT

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Office action dated 26 February 2007, please amend the above-identified application as follows:

Amendments to the claims are reflected in the Listing of Claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 11 of this paper.

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application.

Listing of Claims:

1. (currently amended) A method of correcting impedance curvature in a MOS driver circuit, said method comprising:

using a first MOS transistor and second MOS transistor as part of said MOS driver circuit; and

operating said first MOS transistor and said second MOS transistor so as to compensate for changes in output impedance of said first MOS transistor through corresponding changes in output impedance of said second MOS transistor so as to maintain an output impedance of said MOS driver circuit within a desired tolerance, said operation further comprising:

maintaining a controlled voltage at a first input terminal of said first MOS transistor, and
using a signal adder circuit to provide a differential voltage at a second input
terminal of said second MOS transistor.

2. (cancelled)

3. (cancelled)

4. (currently amended) The method of claim [[2]] 1, further comprising using an amplifier to provide an input signal to said signal adder circuit.

5. (previously presented) The method of claim 4, wherein using said amplifier includes using an output of a differential amplifier to supply said input signal to said signal adder circuit.

6. (cancelled)

7. (currently amended) A method of correcting impedance curvature in a MOS driver circuit, said method comprising:

using a first MOS transistor and second MOS transistor as part of said MOS driver circuit; and

operating said first MOS transistor and said second MOS transistor so as to increase output impedance of said second MOS transistor when output impedance of said first MOS transistor decreases, and to decrease output impedance of said second MOS transistor when output impedance of said first MOS transistor increases so as to maintain an output impedance of said MOS driver circuit within a desired tolerance, said operating further comprising:

maintaining a controlled voltage at a first input terminal of said first MOS transistor, and
using a signal adder circuit to provide a differential voltage at a second input terminal of
said second MOS transistor.

8. (cancelled)

9. (currently amended) The method of claim [[8]] 7, further comprising using a replica of said MOS driver circuit to supply an input signal to said signal adder circuit.

10. (cancelled)

11. (original) The method of claim 9, wherein said operating further comprises:

using an amplifier to supply said controlled voltage to said first MOS transistor and also to a third input terminal of said replica of said MOS driver circuit.

12. (cancelled)

13. (original) The method of claim 11, wherein using said amplifier further comprises using an output of a differential amplifier connected in an inverting configuration to supply said controlled voltage.